Notice of References Cited

Application/Control No.

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Examiner

Monica Lewis

Applicant(s)/Patent Under
Reexamination
YAMAZAKI ET AL.

Art Unit
Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,225,150	05-2001	Lee et al.	438/153
*	В	US-6,288,413	09-2001	Kamiura et al.	257/66
*	С	US-6,259,138	07-2001	Ohtani et al.	257/351
	D	US-			
	Е	US-			
	F	US-			
	G	US-			
	н	US-			
	l i	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP405257137	10-1993	Japan	Sukegawa	G02F001/1335
	0					
	Р					
	Q					
	R					
	s					
	Т					·

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Peter Van Zant, Microchip Fabrication, 2000, McGraw-Hill, Fourth Edition, Page 391.
	٧	S. Wolf, Silicon Processing, 1990, Lattice Press, Volume 2, Page 398.
	w	
	х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.